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Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

O.I.P.E. - PATENT DATE

SCANNED AC³ Q.A. 

APPLICATION NO. 09/910915	CONT/PRIOR F 4	CLASS 714	SUBCLASS 763	ART UNIT 2133	EXAMINER <i>Abram</i>
APPLICANTS Kouji Yamamoto					<i>Abraham</i> <i>E S S a w</i>
TITLE Semiconductor memory device having ECC type error recovery circuit					

PTO-2040
12/99

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	

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